

FIG. 1

FIG. 2

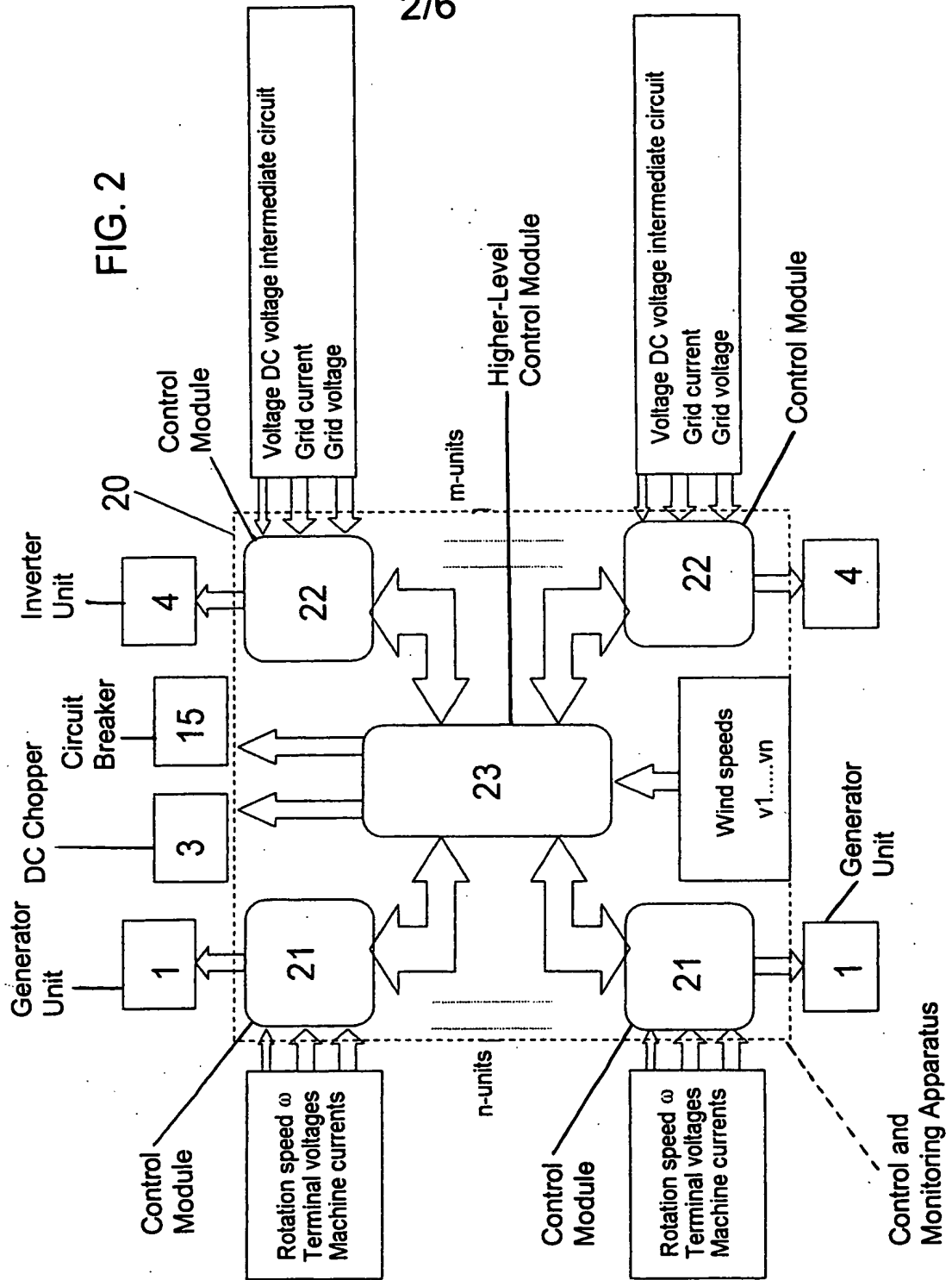
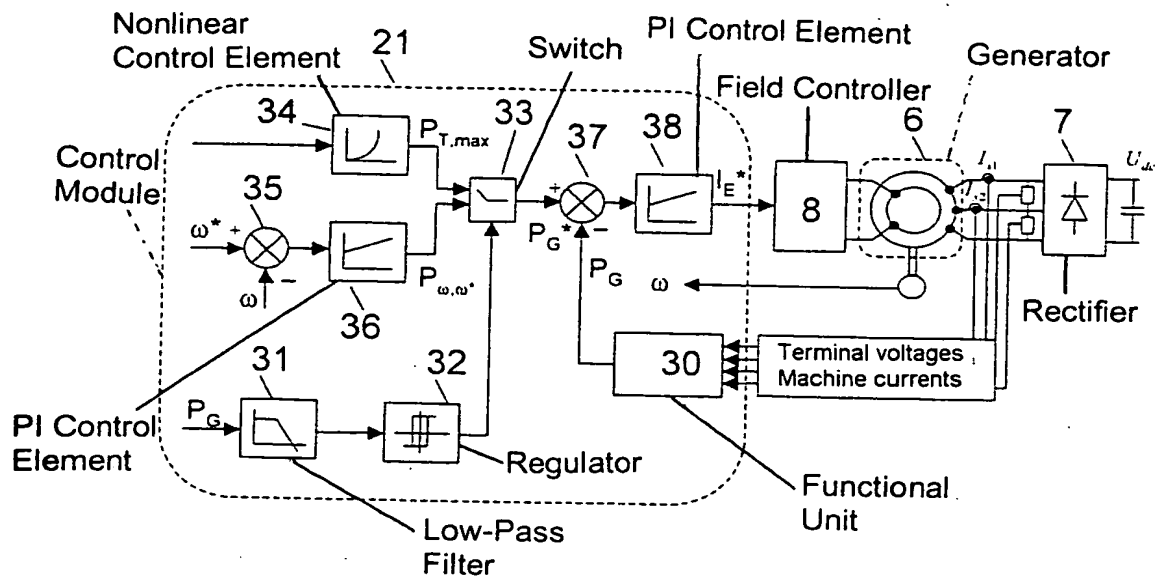


FIG. 3A



Higher-Level Control Module

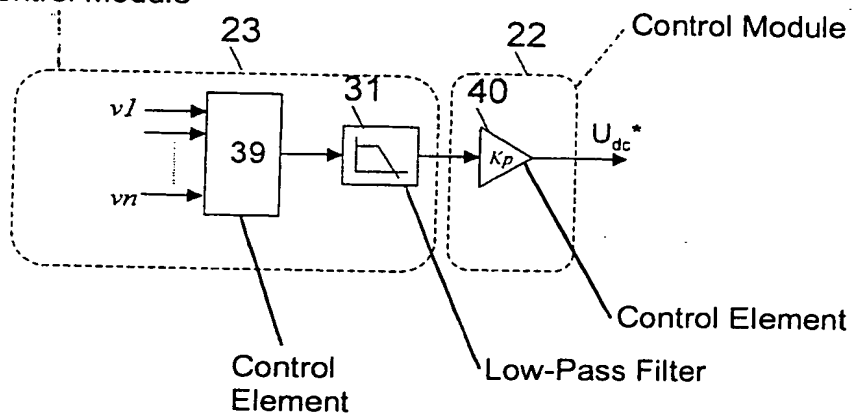


FIG. 3B

FIG. 4

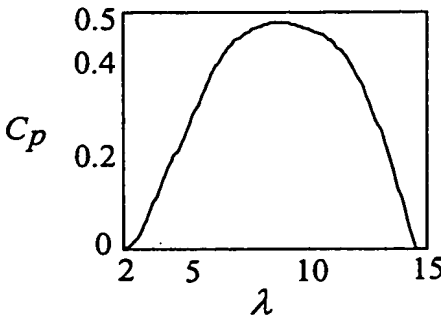


FIG. 5

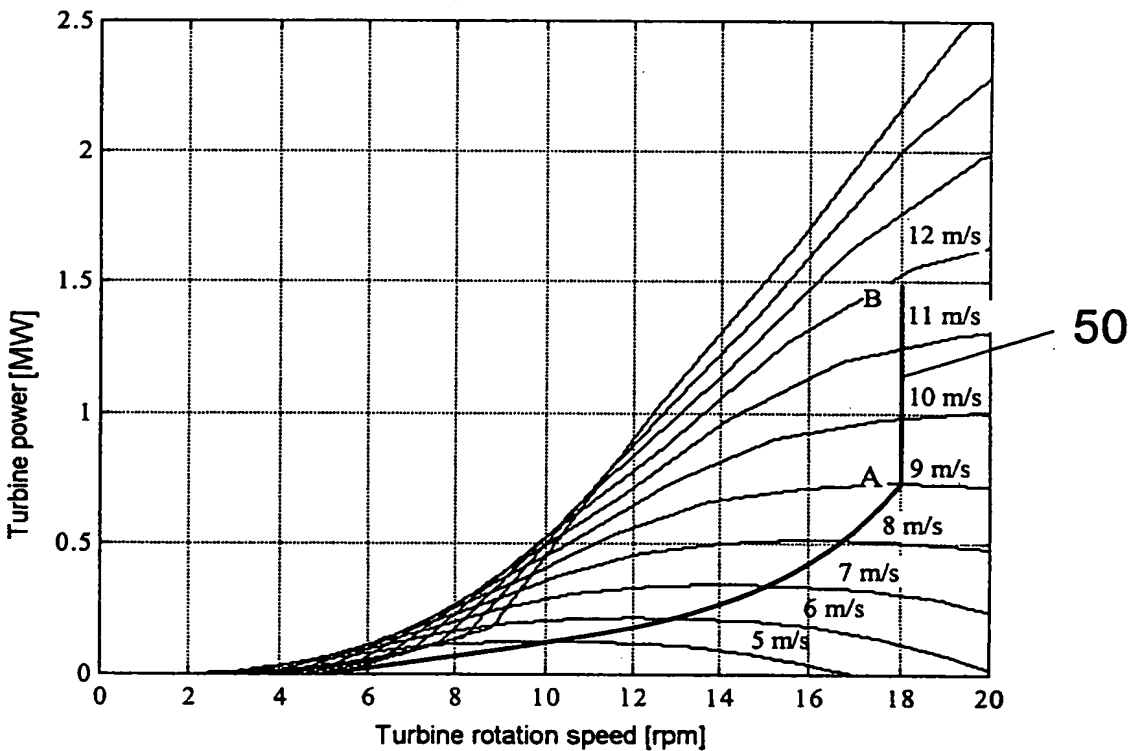


FIG. 6A

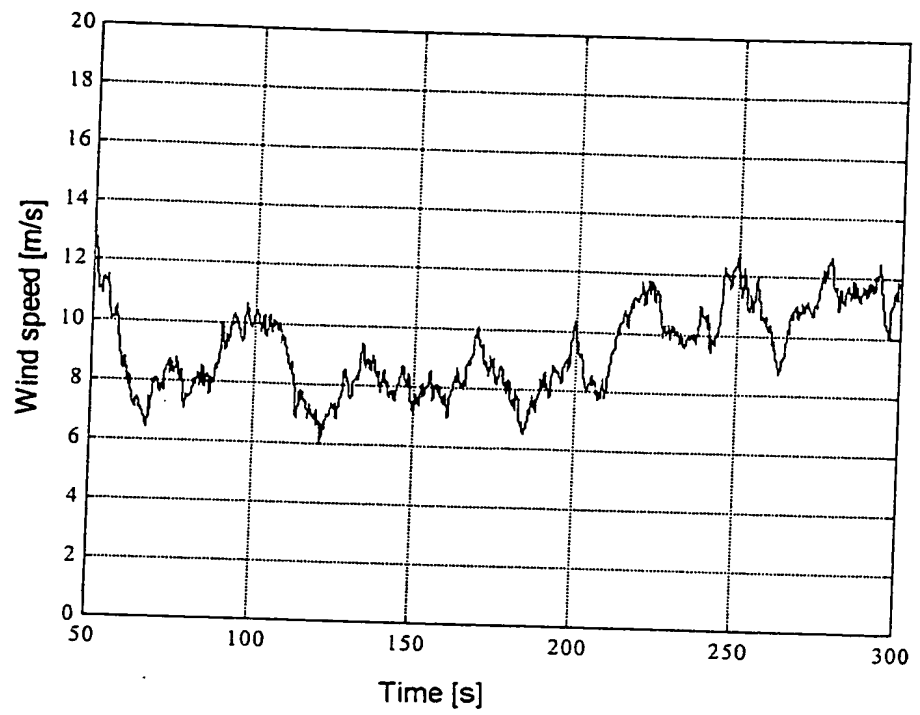


FIG. 6B

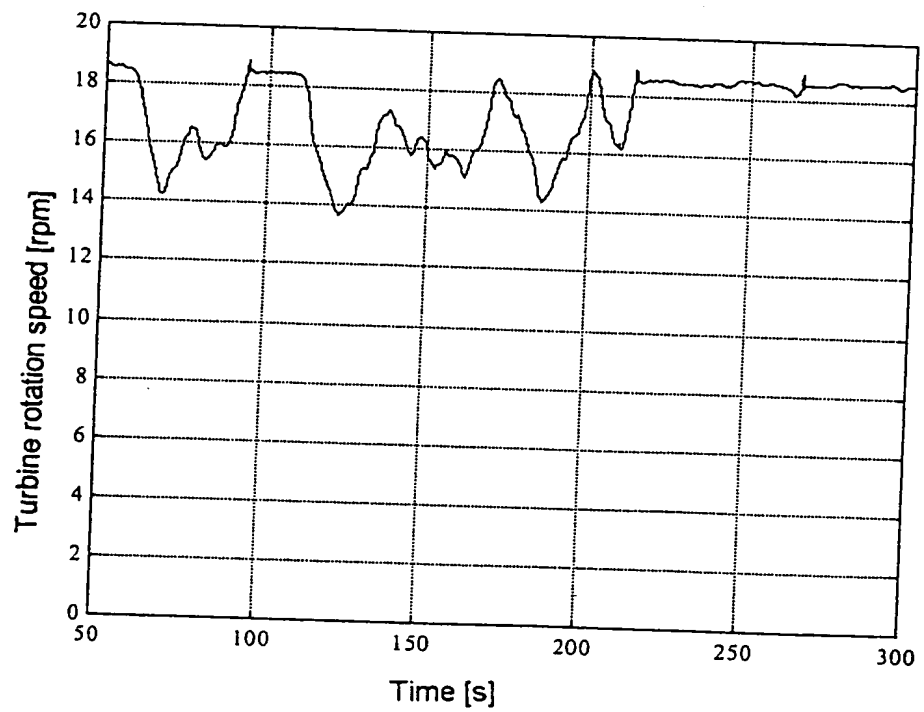


FIG. 6C

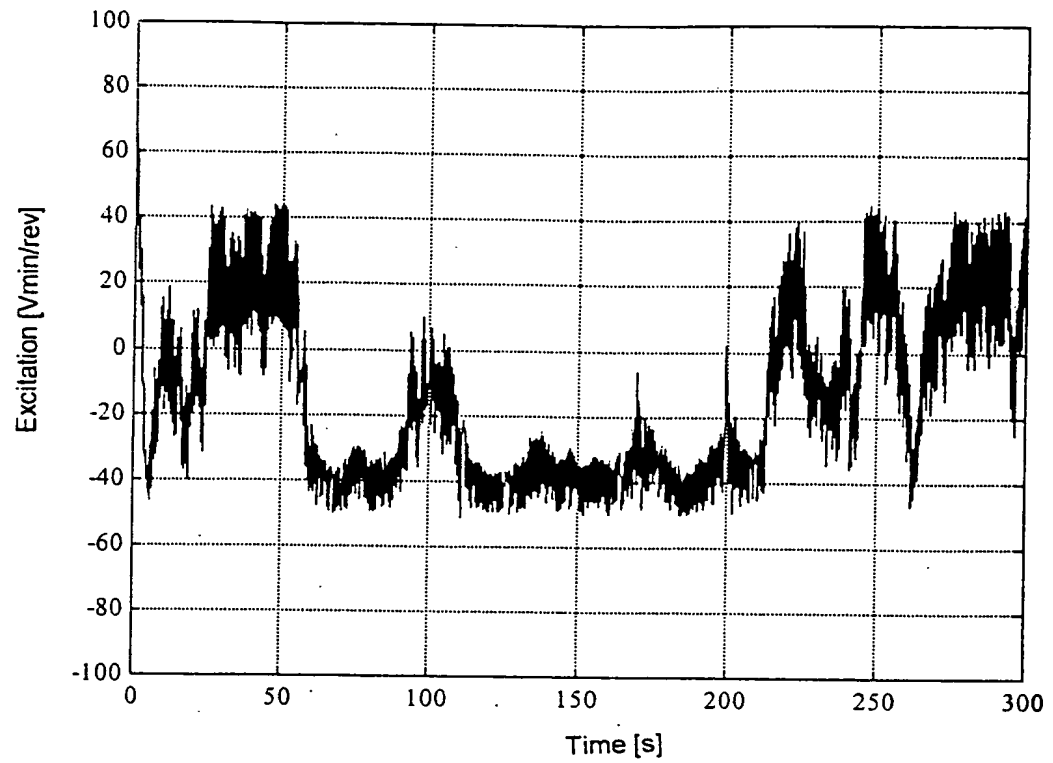


FIG. 6D

